MAYS075GZ

Silicon planar type

For surge absorption circuits

■ Features

- Small terminal capacitance C_t
- High electrostatic discharge ESD

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Total power dissipation *1	P_{T}	150	mW	
Junction temperature	T_{j}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	
Electrostatic discharge *2	ESD	±12	kV	

- Note) $*1: P_T = 150 \text{ mW}$ achieved with a printed circuit board.
 - *2: Test method: IEC61000-4-2

 $(C = 150 \text{ pF}, R = 330 \Omega, \text{Contact discharge: } 10 \text{ times})$

■ Package

- Code
- SSMini2-F4
- Pin Name
 - 1: Anode
 - 2: Cathode
- Marking Symbol: CZ

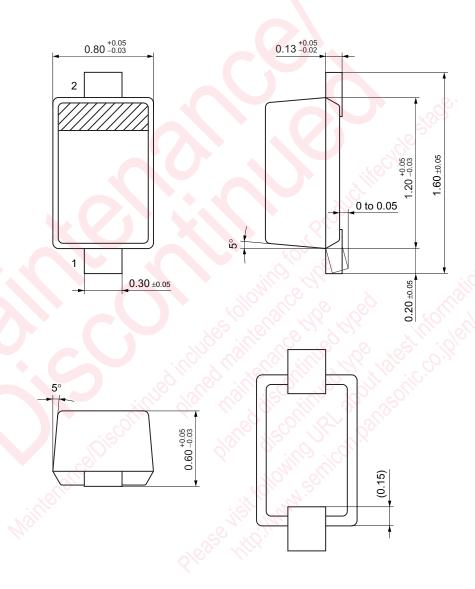
■ Electrical Characteristics $T_a = 25$ °C±3°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Breakdown voltage *	V_{BR}	I _R = 1 mA	6	7.5		V
Reverse current	I_R	$V_R = 5 V$		* IVI	2	μΑ
Terminal capacitance	$C_{\rm t}$	$I_R = 0 \text{ V}, f = 1 \text{ MHz}$	Se X	1.5	3	pF

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

^{2. *:} V_Z guaranted 20 ms after current flow.

SSMini2-F4 Unit: mm



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